

<b>Required Course:</b>	<b>ECE 36500 Introduction to the Design of Digital Computers</b>
<b>Credit and contact hours:</b>	(3 cr.) Class 3, Lab 0
<b>2020-21 IUPUI Campus Bulletin description:</b>	ECE 36500 Introduction to the Design of Digital Computers (3 cr.) P: ECE 36200. Class 3. The hardware organization of computer systems: ARM instruction set architecture, processing unit, pipeline, arithmetic/logic unit design, hardwired and microprogrammed control schemes, memory and cache organization, I/O and interrupt interface design.
<b>Prerequisite or corequisite:</b>	P: ECE 362
<b>Prerequisites by topic:</b>	Digital devices, Number representation and codes, Computer programming.
<b>Textbook:</b>	C. Hamacher, Z. Vranesic, S. Zaky, and N. Manjikian, Computer Organization and Embedded Systems, Sixth Edition, McGraw-Hill, 2011. ISBN: 978-0-07-338065-0
<b>Coordinator:</b>	John Lee, Associate Professor of Electrical and Computer Engineering
<b>Goals:</b>	To teach junior or senior engineering students computer design and analysis techniques beginning with computer organization and ending with performance analysis.
<b>Outcomes:</b>	Upon successful completion of the course, students should be able to <ol style="list-style-type: none"> <li>1. Describe different types of instruction set architecture [1]</li> <li>2. Design a processing unit of a microprocessor [1]</li> <li>3. Describe the concept of processor pipelining and its performance impact [1]</li> <li>4. Describe the memory organization with caches and their performance impact [1]</li> <li>5. Describe the key components of computer systems and their operation [1]</li> <li>6. Design an Arithmetic Logic Unit [1]</li> </ol>
<b>Topics:</b>	<ol style="list-style-type: none"> <li>1. Basic computer organization (1 class)</li> <li>2. Instruction set architectures (4 classes)</li> <li>3. I/O organization and devices (2 class)</li> <li>4. Processing unit organization (3 classes)</li> <li>5. Pipelined processor organization (3 classes)</li> <li>6. Memory/cache organization (6 classes)</li> <li>7. Arithmetic and logic unit design (5 classes)</li> <li>8. Exams (2 classes and final exam period)</li> </ol>
<b>Computer usage:</b>	Computer programming
<b>Laboratory projects:</b>	None.
<b>ABET category:</b>	Engineering science 50% and engineering design 50%
<b>Prepared by:</b>	John Lee
<b>Date:</b>	October 22, 2021

